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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
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	10/654,996 09/05/2003 Hiroki Kanai	UR & BRUNDIDGE, P.C.	ELMORE, REBA I		
	TIE ROILE		ART UNIT	PAPER NUMBER	
ALEXANDRIA, VA 22314 2189		2189			
		DATE MAIL ED: 03/20/2006			

Please find below and/or attached an Office communication concerning this application or proceeding.

,		Application No.	Applicant(s)				
Office Action Summary		10/654,996	KANAI, HIROKI				
		Examiner	Art Unit				
		Reba I. Elmore	2189				
The MAILING DATE of this c Period for Reply	ommunication app	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PER WHICHEVER IS LONGER, FROM - Extensions of time may be available under the after SIX (6) MONTHS from the mailing date of - If NO period for reply is specified above, the mailing to reply within the set or extended perion Any reply received by the Office later than three earned patent term adjustment. See 37 CFR 1	THE MAILING DA provisions of 37 CFR 1.13 this communication. aximum statutory period w d for reply will, by statute, e months after the mailing	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be tim rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	l. ely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status			•				
1) Responsive to communicatio	n(s) filed on 10 M	arch 2006					
2a)☐ This action is FINAL .	· ·	action is non-final.					
<u> </u>	• • • • • • • • • • • • • • • • • • • •	ice except for formal matters, pro	secution as to the merits is				
		x parte Quayle, 1935 C.D. 11, 45					
Disposition of Claims							
4)⊠ Claim(s) <u>1-20</u> is/are pending	in the application						
4a) Of the above claim(s)							
5) Claim(s) is/are allowed							
6)⊠ Claim(s) <u>1-20</u> is/are rejected.	•		•				
7) Claim(s) is/are objected							
8) Claim(s) are subject to		election requirement.	• .				
Application Papers							
			•				
9)☐ The specification is objected t	-						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.							
		drawing(s) be held in abeyance. See					
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11)☐ The oath or declaration is obj	ected to by the Ex	aminer. Note the attached Office	Action or form P1O-152.				
Priority under 35 U.S.C. § 119							
12)☐ Acknowledgment is made of a a)⊠ All b)☐ Some * c)☐ Nor	_	priority under 35 U.S.C. § 119(a)	-(d) or (f).				
1. Certified copies of the	•		•				
	2. Certified copies of the priority documents have been received in Application No						
	3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the Int		` ','					
* See the attached detailed Office action for a list of the certified copies not received.							
		•	,				
Attachment(s)	•						
1) Notice of References Cited (PTO-892)		4) Interview Summary	(PTO-413)				
2) 🔲 Notice of Draftsperson's Patent Drawing R		Paper No(s)/Mail Da	te				
3) M Information Disclosure Statement(s) (PTO Paper No(s)/Mail Date 1/31/05.	-1449 or PTO/SB/08)	5) Notice of Informal P	atent Application (PTO-152)				

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DETAILED ACTION

1. Claims 1-20 are presented for examination.

SPECIFICATION

2. The specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.
- 4. Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Yoshida.
- 5. Yoshida teaches the invention (claim 1) as claimed including a storage device control apparatus comprising:

a mounting part for removable memory as disk memory storage systems (e.g., see Figure 1);

channel control units with a host interface controller for receiving data I/O requests (e.g., see element 11 of Figure 1);

disk control units which have disk interface controllers for performing I/O control of the data to storage volumes in response to the data I/O requests (e.g., see Figure 1);

cache memory units (e.g., see element 14 of Figure 1);

storage control units having a host interface controller, disk interface controller and memory (e.g., see paragraph 0010 and Figures 2-3); and,

an internal connection part for connecting the channel control units, the disk control units, the cache memory units and the storage control units (e.g., see Figures 1-4).

As to claim 2, Yoshida teaches information for specifying the storage volumes specified by the disk control unit which performs I/O control and information to which another storage control unit performs I/O control is stored in the memory of the storage control unit (e.g., see paragraph 0021).

As to claim 3, Yoshida teaches a first storage area for storing the data in storage volumes for the storage control unit performing I/O control (e.g., see paragraph 0021 and paragraphs 0037-0039 and Figure 4); a second storage area for storing the data to be stored in the storage volumes for the storage control unit performing the I/O control (e.g., see paragraph 0021 and paragraphs 0037-0039 and Figure 4) and information for specifying the first and second storage areas are stored in the memory of the storage control unit (e.g., see paragraph 0021 and paragraphs 0037-0039 and Figure 4).

As to claim 4, Yoshida teaches the storage control unit includes a communication buffer for storing data exchanged with another storage control unit as the transfer control unit (e.g., see Figure 4).

6. Yoshida teaches the invention (claim 5) as claimed including a control method for a storage device control apparatus including:

a mounting part for removably mounting a memory device as disk memory storage systems (e.g., see Figure 1);

channel control units having a host interface controller for receiving data I/O requests as channel interface units (e.g., see Figure 4);

disk control units which have a disk interface controller for performing I/O control of the data to storage volumes storing data in response to the data I/O requests (e.g., see Figure 4) cache memory units (e.g., see Figure 4);

storage control units having a host interface controller, the disk interface controller and memory (e.g., see Figure 2-3 and paragraph 0010);

an internal connection part for connection part for connecting the channel control unit, the disk control unit, the cache memory unit and the storage control unit (e.g., see Figures 1-4);

a plurality of storage control units which hold information for identifying a unit to perform I/O control to a storage volume in each memory with each I/O request is directed to a mounted disk (e.g., see Figures 1-4);

receiving the data I/O request at one of the storage control units (e.g., see Figure 4 and paragraphs 0037 to 0043);

referring to the information by the storage control unit to identify the unit to perform I/O control to the storage volume to which the data I/O request is directed (e.g., see Figure 4 and paragraphs 0037-0043); and,

performing the I/O control by the storage control unit when the unit to perform the I/O control is the storage control unit, or letting another storage control unit perform the I/O control when the unit to perform the I/O control is not the storage control unit (e.g., see Figure 4 and paragraphs 0037-0043).

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As to claim 6, Yoshida teaches sending the read request to another storage control unit by the storage control unit; performing the I/O control in response to the read request by another storage control unit; and sending the received data to an information processing apparatus by the storage control unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 7, Yoshida teaches sending the write request and write data to another storage control unit by the storage control unit and performing the I/O control of the write data in response to the write request by another storage control unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 8, Yoshida teaches receiving the data from another storage control unit data indicating the completion of the I/O control of the write data (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 9, Yoshida teaches sending the read request to another storage control unit by the storage control unit; writing the read request into a buffer in another storage control unit; reading the read request into a buffer of another storage control unit by the original unit; receiving data from another control unit by the storage control unit by reading the data written by another storage control unit in the buffer provided in the storage control unit by the storage control unit (e.g., see Figure 4 and paragraphs 0037-0043);

As to claim 10, Yoshida teaches the buffer for storing data exchanged with another storage control unit; sending the write request and write data to another storage control unit by the storage control unit; writing the write request and the write data into a buffer provided in another storage control unit by the storage control unit and reading the write request and the

write data from the buffer to another storage control unit by the same storage control unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 11, Yoshida teaches receiving data indicating the I/O control of the write data has been completed form another storage control unit by the storage control unit by reading the data written by another storage control unit in the buffer provided in the storage control unit by the same storage control unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 12, Yoshida teaches receiving the data I/O request by the storage control unit; referring to the information by the storage control unit to identify the unit to perform the I/O control to the storage volume for the directed I/O request; and performing the I/O control by the storage control unit when the unit to perform the I/O control is the storage control unit or performing the I/O control by the disk control unit when the unit to perform the I/O control is not the storage control unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 13, Yoshida teaches sending the read request to the disk control unit by the storage control unit; performing the I/O control in response to the read request by the disk control unit; receiving data from the disk control unit by the storage control unit and sending the received data to an information processing apparatus by the storage control unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 14, Yoshida teaches sending the write request and write data to the disk control unit by the storage control unit and performing the I/O control of the write data in response to the write request by the storage control unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 15, Yoshida teaches a first storage area for storing the data to be stored in the storage volumes for performing I/O control; a second storage area for storing the data to be stored in the storage volumes for the disk control unit performing I/O control; when the storage control unit performs I/O control of the data to the first storage area or when the unit to perform the I/O control is not the storage control unit, the disk control unit performs the I/O control of the data to the second storage area (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 16, Yoshida teaches receiving the data I/O request by one of the storage control units; referring to the information by the storage control unit to identify the unit to perform the I/O control to the storage volume to which the data I/O request is directed (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 17, Yoshida teaches a first storage area for storing the data to be stored in the storage volumes for performing I/O control; a second storage area for storing data in the storage volumes performing I/O control with the control method receiving a data I/O request by the storage control unit, referring to the information by the storage control unit to identify the unit performing the I/O control to the storage volume for the directed data I/O request, performing the I/O control to the first storage area by the storage control unit when the unit for perform the I/O control is the storage control unit and performing the I/O control to the storage volume by the storage control unit when the data is not stored in the cache memory unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to claim 18, Yoshida teaches a first storage area for storing the data to be stored in the storage volumes to which the storage control unit performs I/O control, the second storage area for storing the data to be stored in the storage volumes to which the disk control unit performs

I/O control with the control method receives a data I/O request by the storage control unit, referring to the information by the storage control unit to identify the unit to perform the I/O control to the storage volume to which the data I/O request is directed, performing the I/O control to the second storage area by the storage control unit when the unit to perform the I/O control is the disk control unit performing the I/O control to the cache memory unit by the storage control unit when the data is not stored in the second storage disk and perform the I/O control to the storage volume by the storage control unit when the data is not stored in the cache memory unit (e.g., see Figure 4 and paragraphs 0037-0043).

As to the claim 19, Yoshida teaches duplicating information stored in the memory of each of the storage control units by each of the storage control units, for identifying the unit to perform the I/O control to the storage volume to which the data I/O request is directed and write the duplicated information into the cache memory unit, referring to the information by the storage control unit to identify the unit to perform the I/O control to the storage volume to which the I/O request is directed by referring to the information in the cache memory unit by the storage control unit to identify the unit when the storage control unit cannot identify the unit merely by consulting the information stored in the memory of the storage control unit (e.g., see Figure 4 and paragraphs 0019 and 0037-0043).

As to claim 20, Yoshida teaches duplicating the data stored in the storage volume to which the storage control unit performs I/O control by one of the storage control units, write the duplicated data into the storage volume to which the disk control unit performs I/O control and changing the information, stored in the respective memories of the storage control unit and

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another storage control unit for identifying the unit to perform the I/O control, from the storage control unit to the disk control unit (e.g., see Figure 4 and paragraphs 0019 and 0037-0043).

CONCLUSION

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Reba I. Elmore, whose telephone number is (571) 272-4192. The examiner can normally be reached on Monday or Wednesday from 7:30am to 6:00pm, EST.

If attempts to reach the examiner by telephone are unsuccessful, the art unit supervisor for AU 2189, Reginald G. Bragdon, can be reached for general questions concerning this application at (571) 272-4204. Additionally, the official fax phone number for the art unit is (571) 273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Tech Center central telephone number is (571) 272-2100.

Reba I. Elmore.

Primary Patent Examiner

Rha l. EM

Art Unit 2189

Sunday, March 19, 2006